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Phone Conference P1450
Lockup: This is a pre-defined cell type that is used to identify "**lockup** latches" which are placed in the **scan chain** typically for the purpose of ...
grouper.ieee.org/groups/ 1450/dot1/minutes-2003-11-06.html - Similar pages

DS21Qx5y BSDL **Scan Chain** Mapping - Maxim/Dallas
JTRST, JTCLK, and JTMS are wired together in **parallel**. To complete the **scan chain**, four copies of the specific DS21x5y BSDL file need to be placed in the ...
www.maxim-ic.com/appnotes.cfm/appnote_number/406 - 78k - Cached - Similar pages

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Lockup latches are nothing more than transparent latches. You use them to connect **two** scan-storage elements in a **scan chain** in which excessive clock skew ...
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> 10 tips for successful scan design: part **two** - 2/17/2000 - EDN
> The first problem is within a design block that contains **multiple** drivers ... Therefore, you should insert a **lockup** latch in the **scan chain** before and after ...
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On-time Finish Rests With **Multiple** Clocks
This means that if **multiple** clocks are used within the same **scan chain**, ... In this example, each **scan chain** has **two** scan cells, and there is a total of ...
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in **two** steps: the first one consists in determining the. chaining of the scan cells so as to ... **multiple** clock domains and **lockup** latches. Also, work ...
itcprogramdev.org/itc2003proc/Papers/PDFs/0018_3c.pdf - Similar pages

Synopsys Physical Scan Synthesis Technology Backgrounder
Typically, to have **multiple** scan chains in a design, the scan cells are ... In the Physical Compiler environment, **scan-chain** allocation within the same ...
www.synopsys.com/products/test/pss_bkgrd.html - 23k - Cached - Similar pages

Applying Advanced Fault Models
These **lock-up** latches eliminate clock skews between domains while shifting through the **scan chain**. These delay the data for half a clock cycle, ...
www.evaluationengineering.com/ archive/articles/0304/0304fault_models.htm - 21k - Cached - Similar pages

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scan chain with minimum test power. To tackle this NP-. hard problem efficiently, the heuristic ... **multiple** clock domains and **lockup** latches. Also, work ...
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Details and datasheet on part: 5962-9475001QXA
Scan data returning from the **scan chain** is placed on the **parallel** port to be read by the host processor. Up to **two** scan chains can be directly controlled ...
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clock domain and scan insertion - comp.cad.synthesis - Apr 20, 2000
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